



S/N 10/772,606

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Joseph E. Geusic et al. Examiner: Julio J. Maldonado  
Serial No.: 10/772,606 Group Art Unit: 2823  
Filed: February 5, 2004 Docket: 303.390US4  
Title: INTEGRATED CIRCUITS USING OPTICAL FIBER INTERCONNECTS  
FORMED THROUGH A SEMICONDUCTOR WAFER

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**SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. § 1.312**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

*Entered  
11/14/2005*  
In response to the Notice of Allowance and Notice of Allowability mailed July 28, 2005,  
please amend as follows: